## What is claimed is: (U)

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| 1  | 1. In a semiactive radar guidance system for a       |
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| 2  | guided missile, such system including a heterodyne   |
| 3  | receiver wherein the frequency of a first local      |
| 4  | oscillator is required to be rendered coherent with  |
| 5  | the frequency of echo signals from a target being    |
| 6  | tracked by a frequency control signal from automatic |
| 7  | frequency control circuitry, such circuitry being    |
| 8  | susceptible to vibration during flight to assume     |
| 9  | one of two stable conditions, the first of such      |
| 10 | conditions being one in which the frequency control  |
| 11 | signal has the proper amplitude and polarity to      |
| 12 | maintain coherency and the second of such conditions |
| 13 | being one in which the polarity of the frequency     |
| 14 | control signal has an incorrect polarity, the        |
| 15 | improvement comprising:                              |
| 16 | (a) first means, responsive to vibration-induced     |
| 17 | changes in the automatic frequency control           |
| 18 | circuitry from one stable condition to the           |
|    |  |

- other, for generating a control signal indicative of such change; and
- second means, responsive to the control (b) signal, for correcting the polarity of the frequency control signal.



| 1 | (E) 2. The improvement as in claim 1 wherein the     |
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| 2 | first means is a differentiator responsive to change |
| 3 | in the polarity of the frequency control signal.     |

1 The improvement as in claim 2 wherein the second means comprises a phase lock loop incorporating 2 the combination of a synchronous detector having a 3 first and a second input and an output terminal, a voltage controlled oscillator and a narrow band 5 6 summing amplifier, a signal representative of a target being tracked being applied to the first input 7 terminal, the output signal of the voltage controlled 8 oscillator being connected to the second input 9 terminal, the summing amplifier being disposed in 10 . circuit between the output terminal and the voltage 11 controlled oscillator with the control signal applied 12 13 to a second input terminal of such amplifier.

PJM:mm